

FIG.1

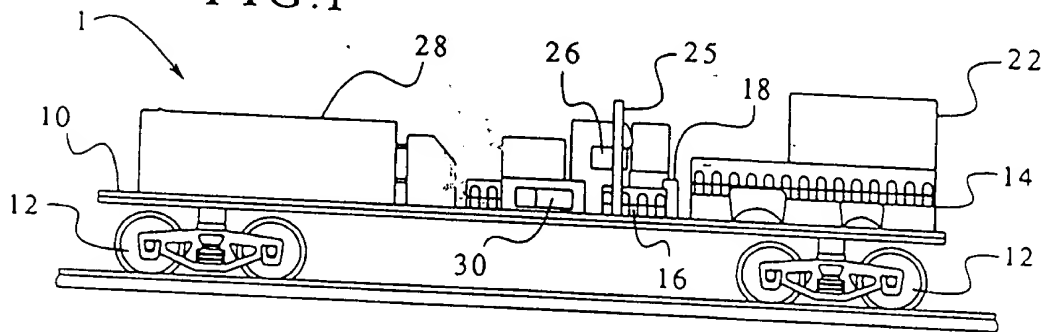


FIG. 2

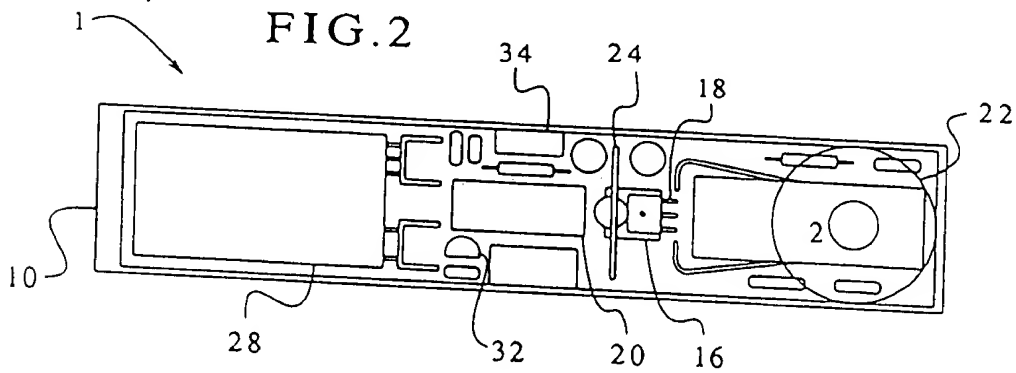
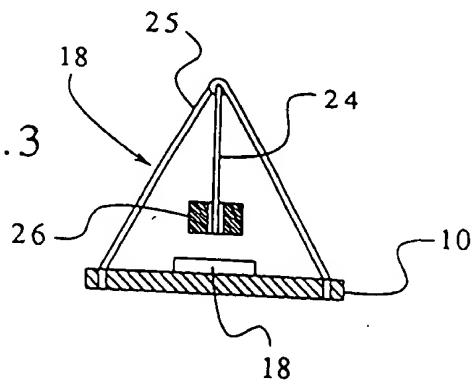
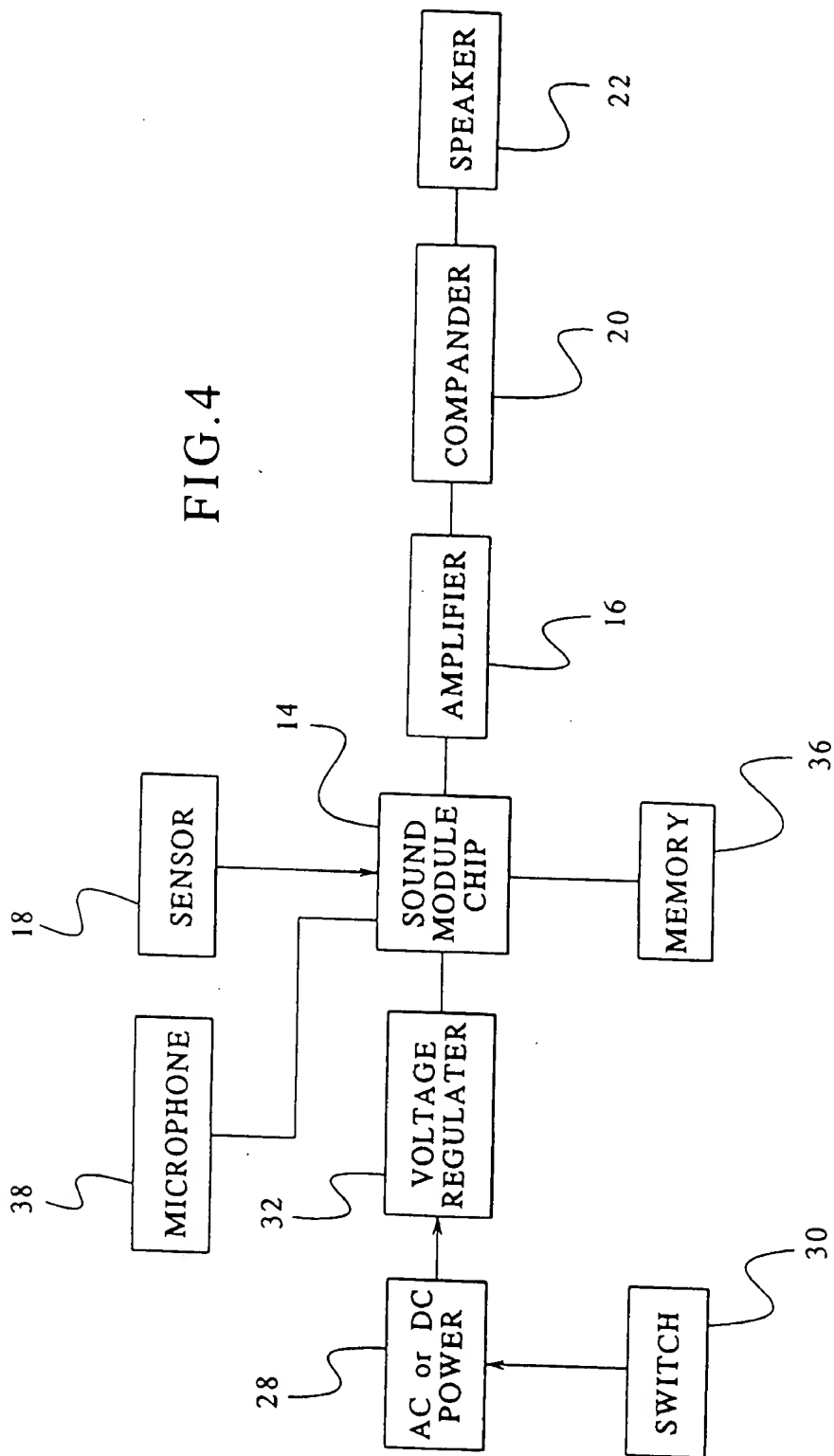
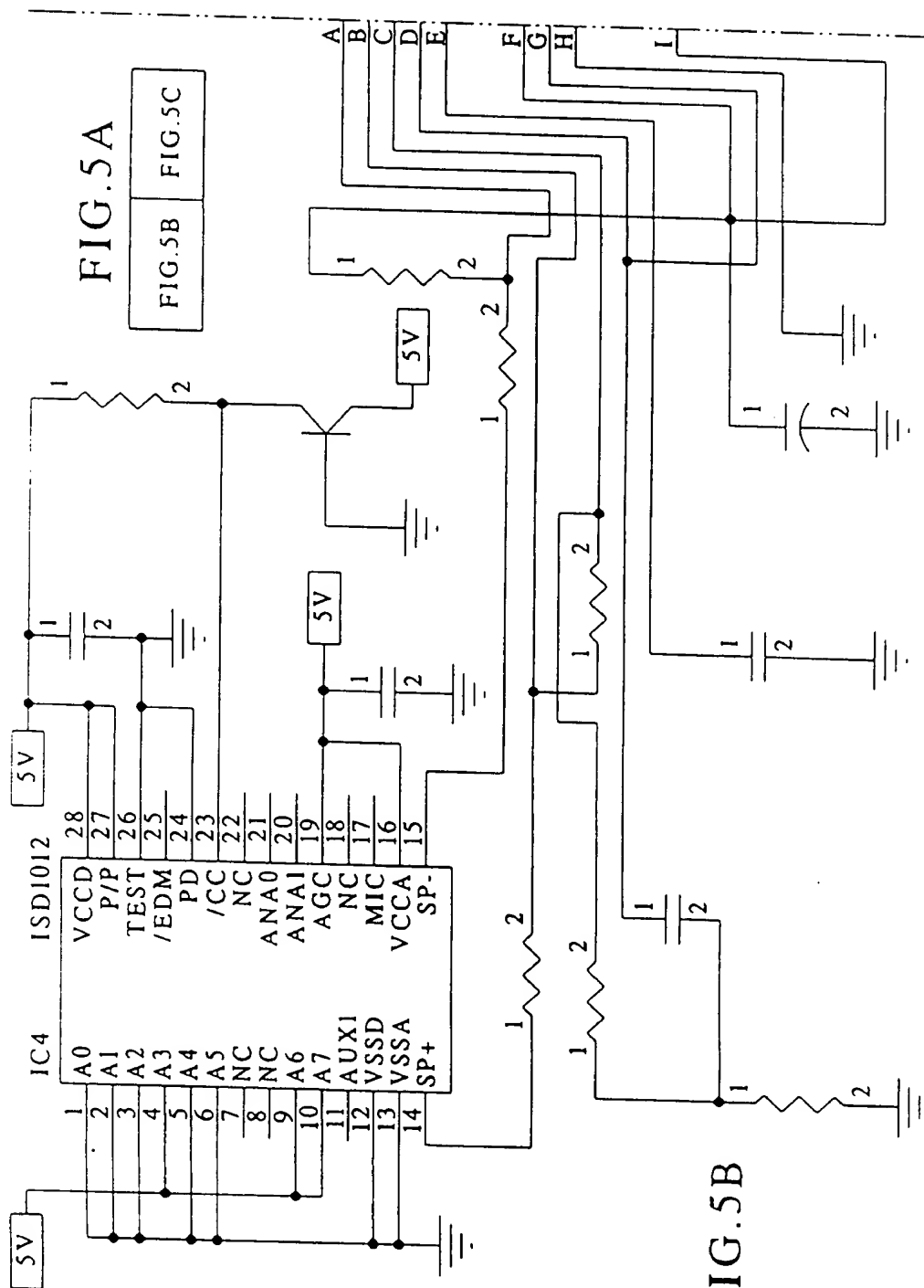


FIG.3







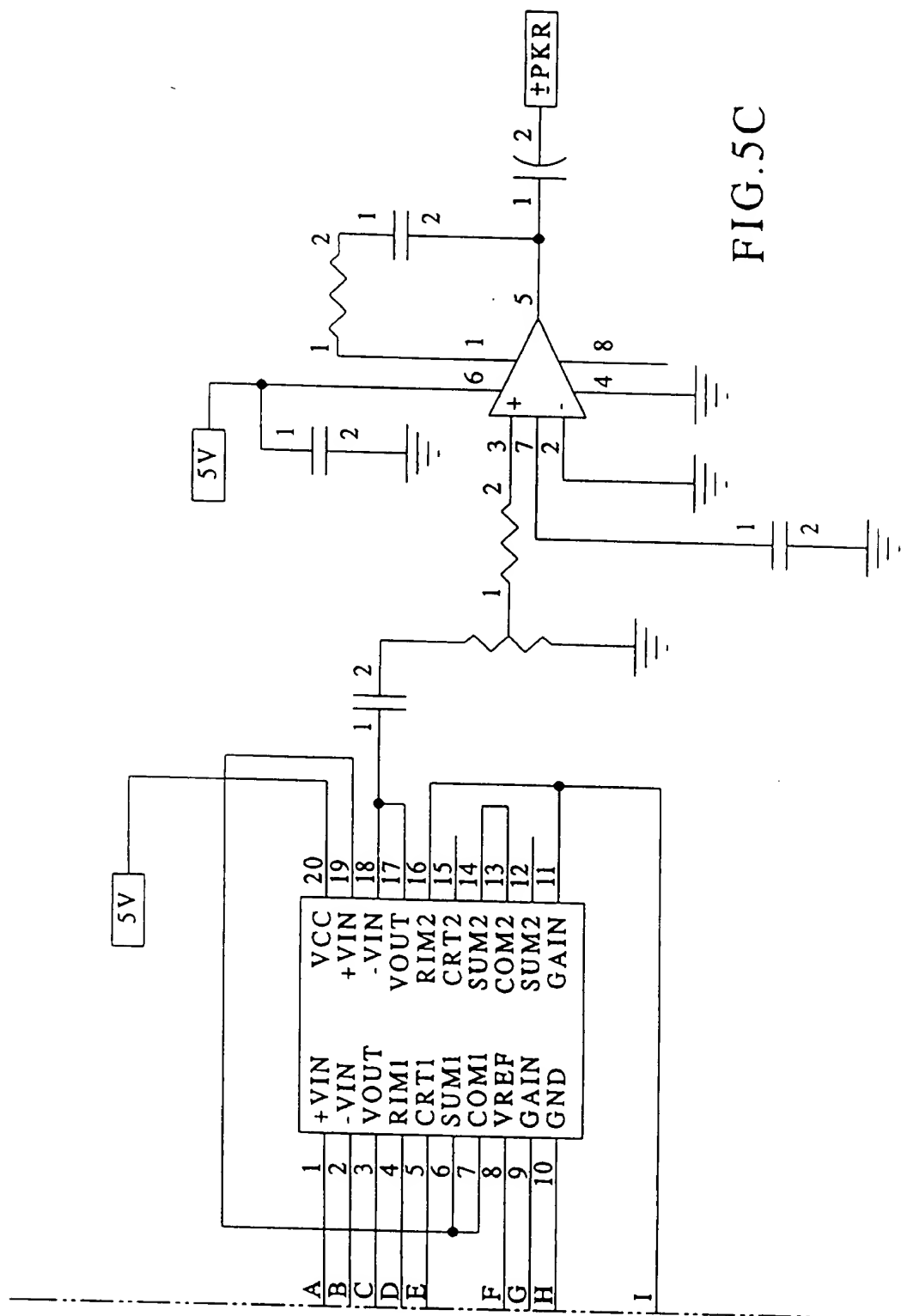
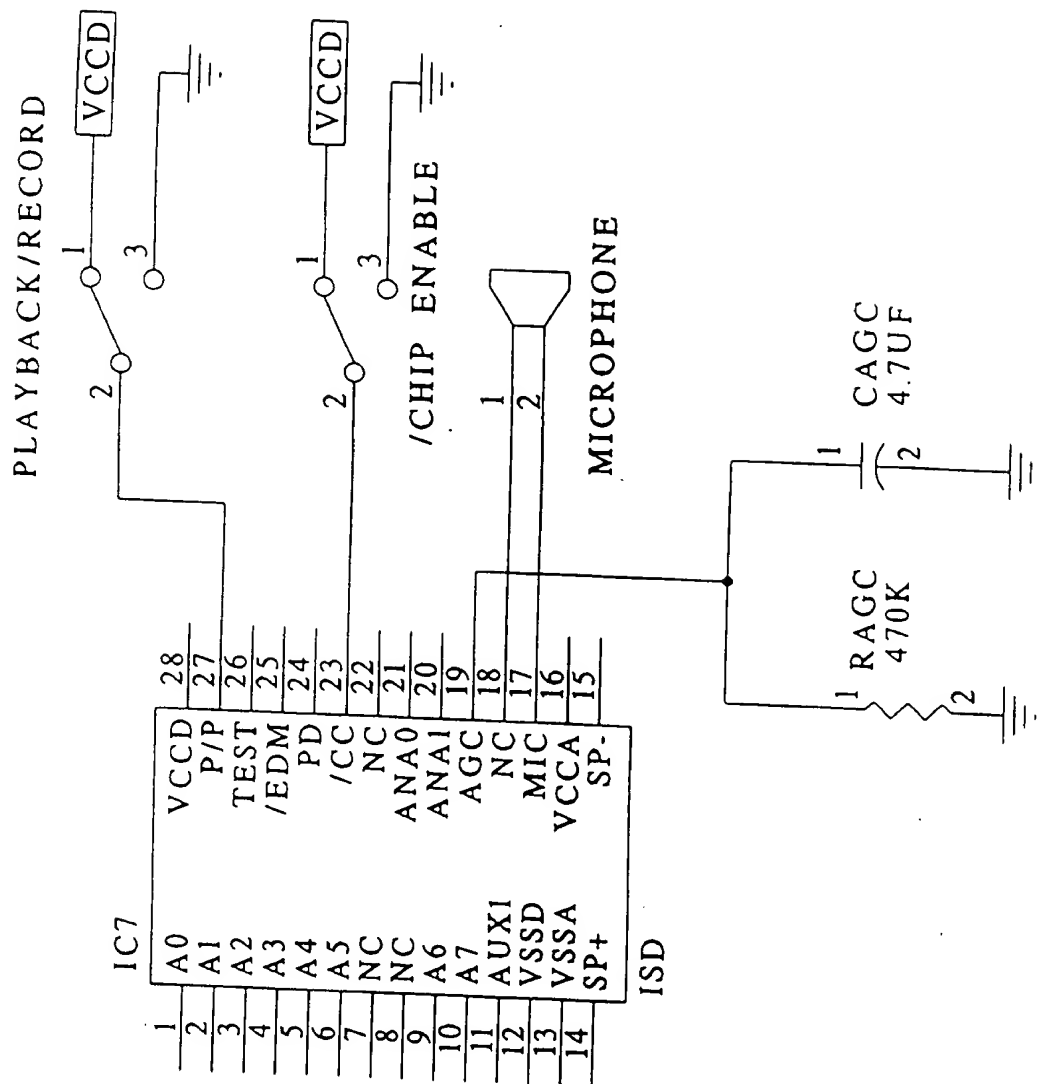


FIG. 5C

FIG.6



[illegible]

FIG. 7B	FIG. 7C
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FIG. 7B

[illegible]

FIG.7C

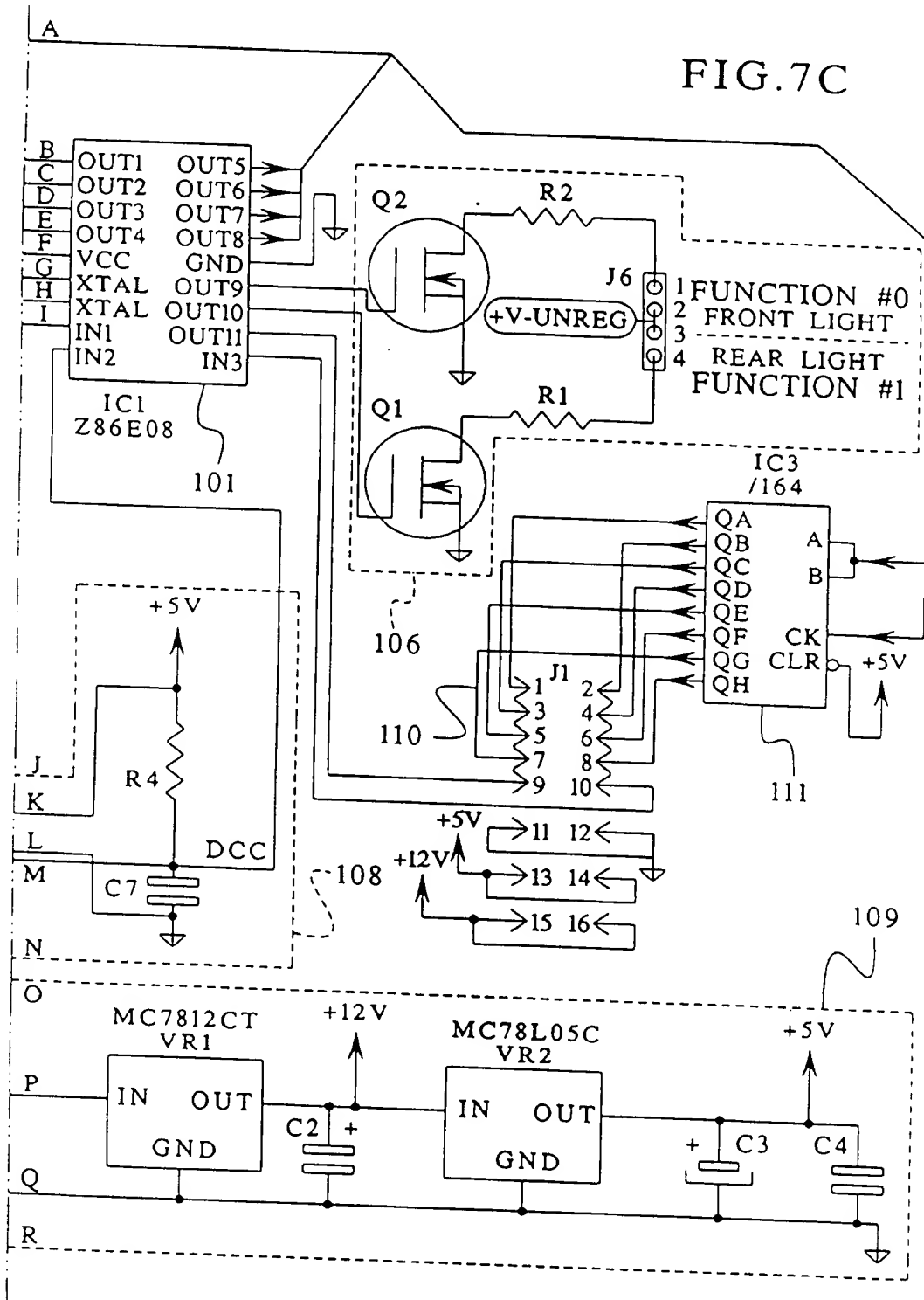
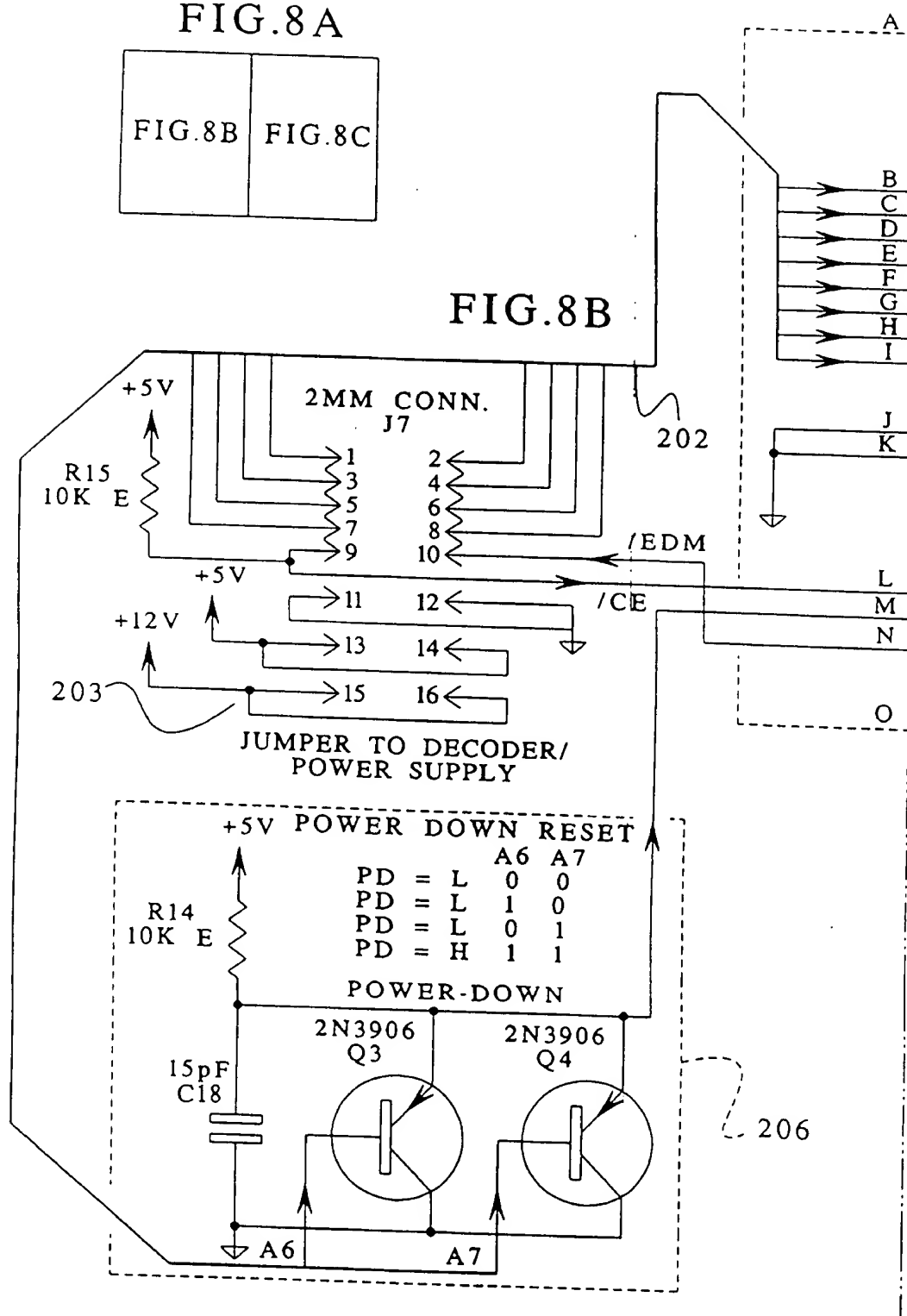


FIG.8B	FIG.8C
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00000-1-970000

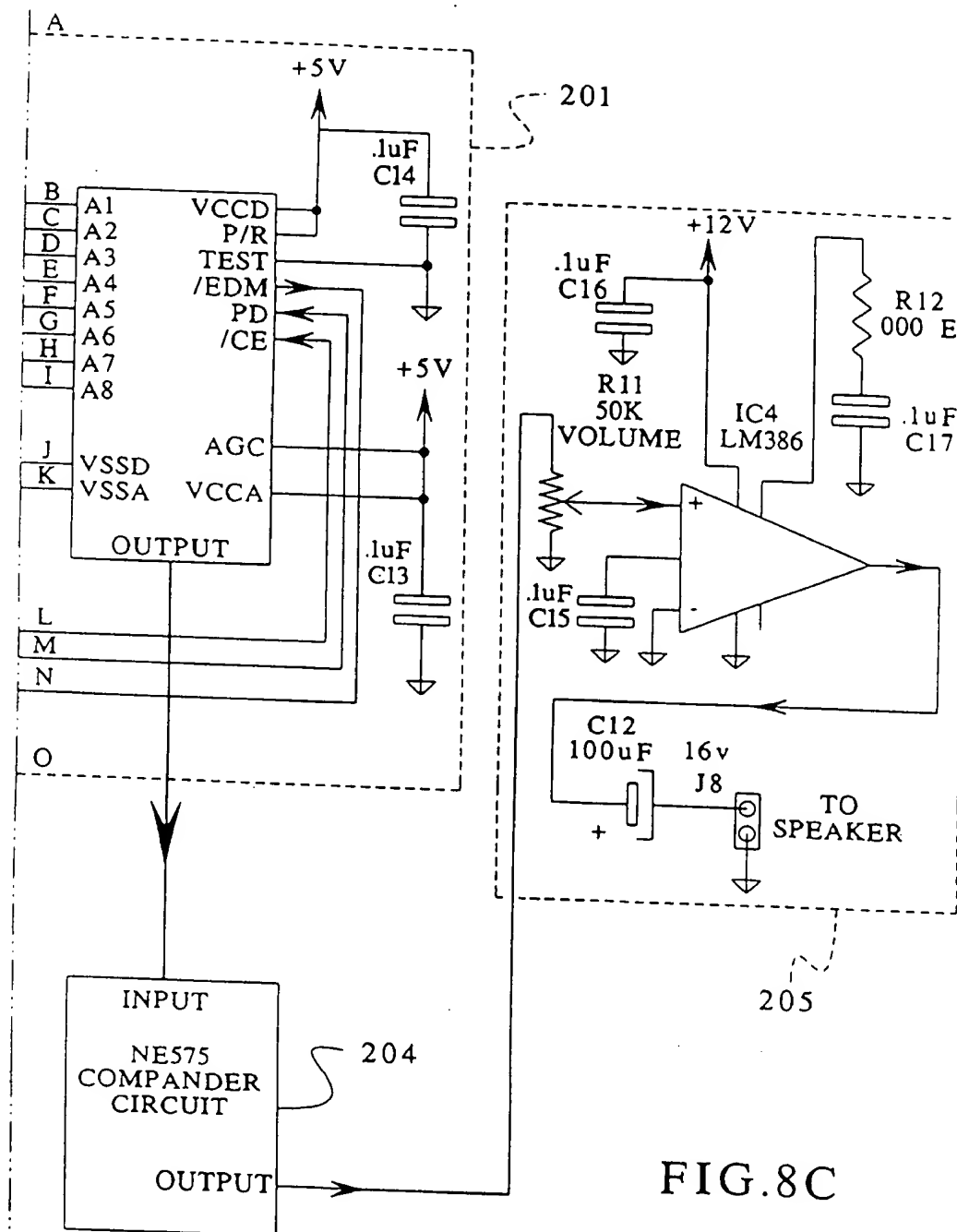


FIG. 8C

FIG.9A

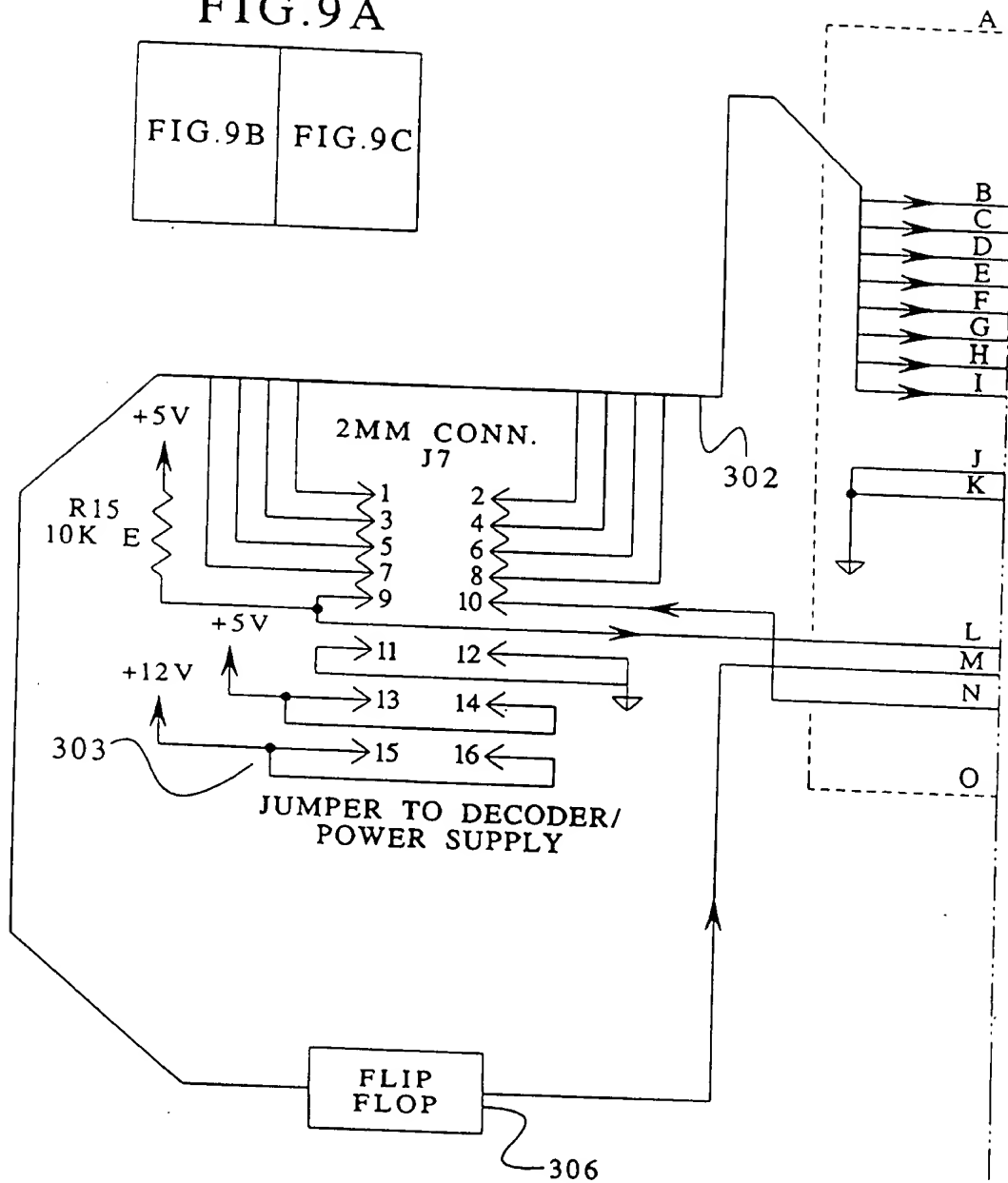
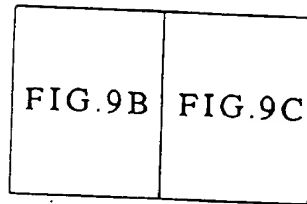


FIG.9B

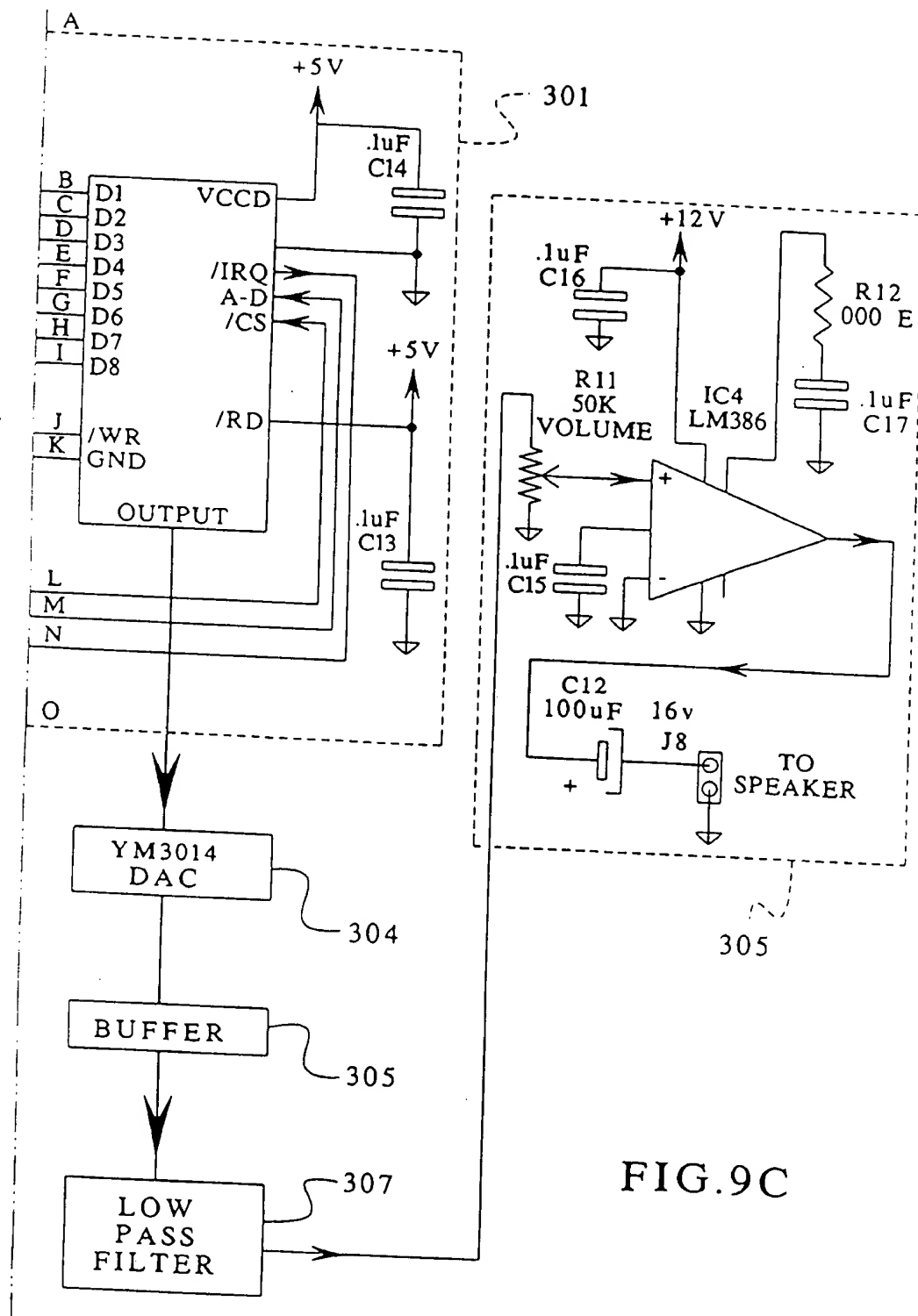


FIG. 10

DCC BASELINE PACKET 401
 1 = 58us (52us-64us) 402
 0 > 100us (90us-1000us) 403

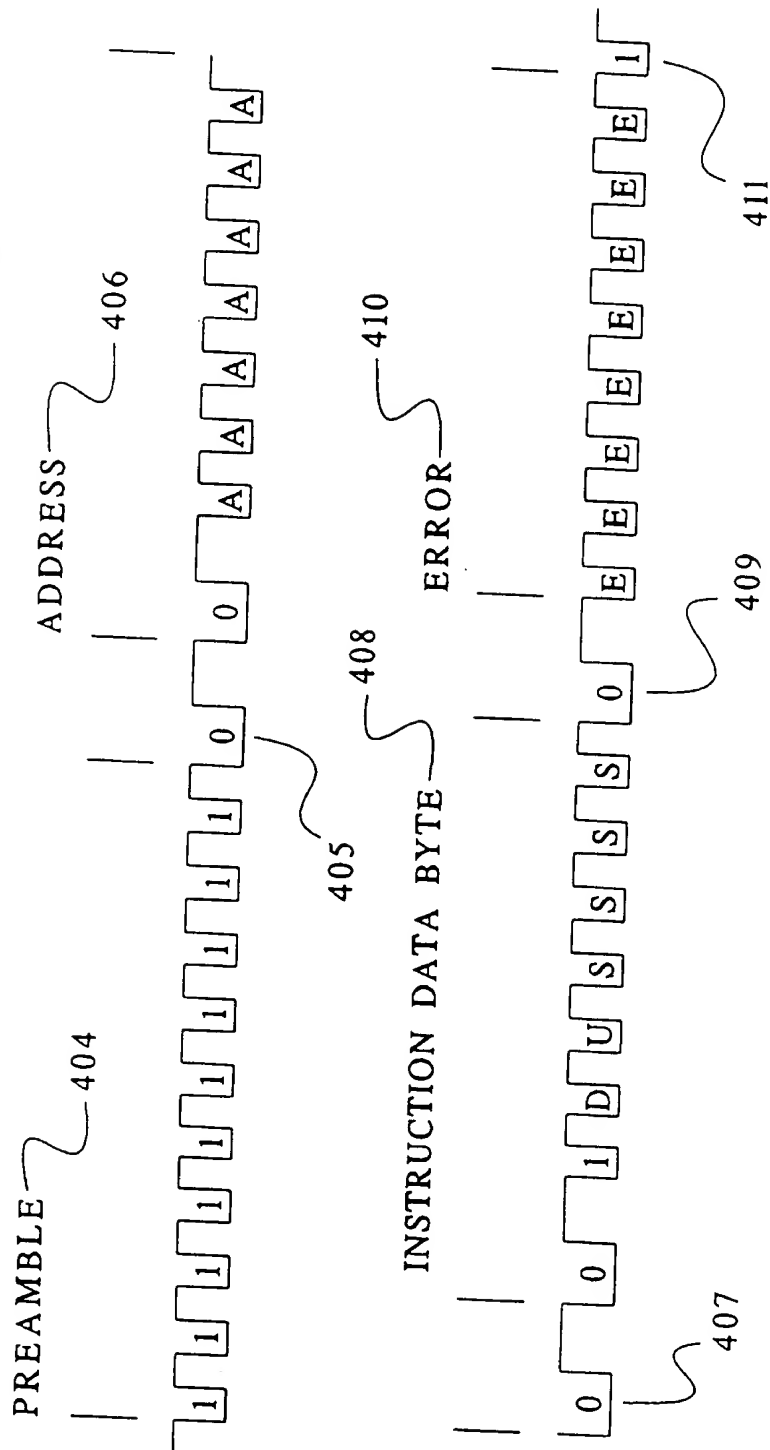


FIG.11

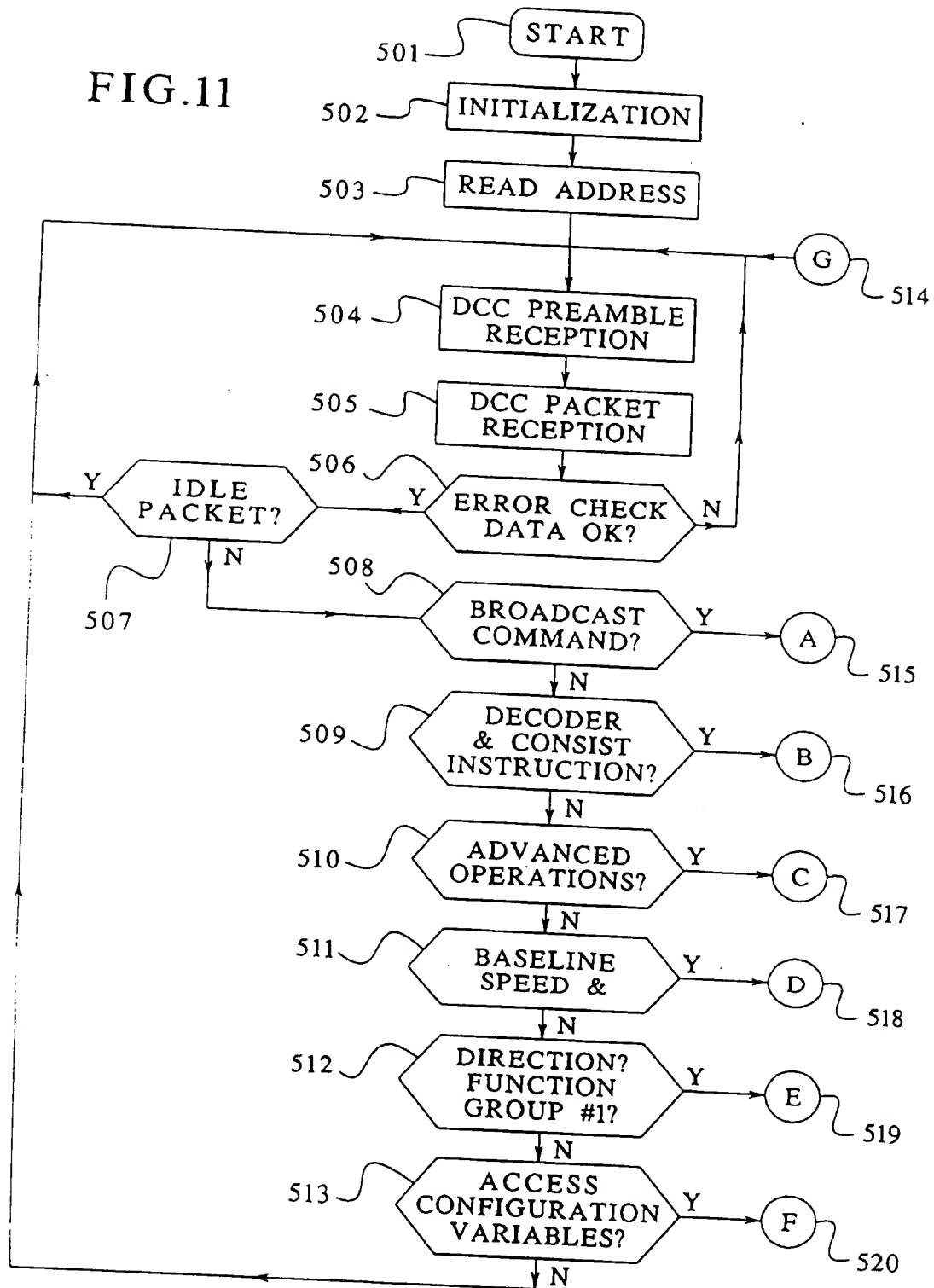


FIG.12

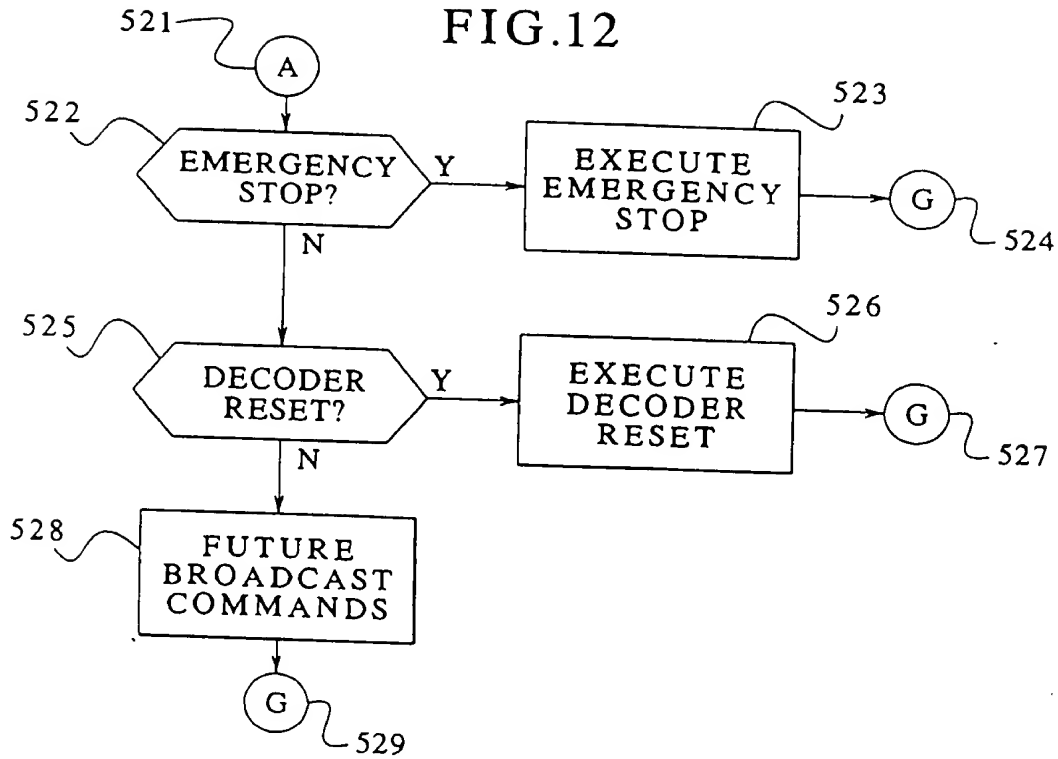


FIG.13

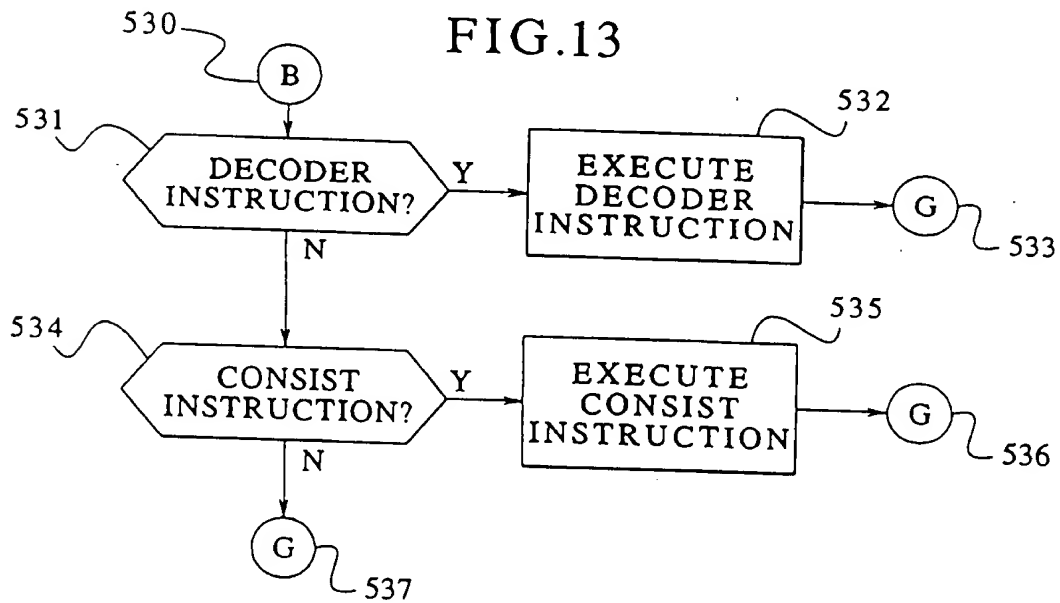


FIG.14

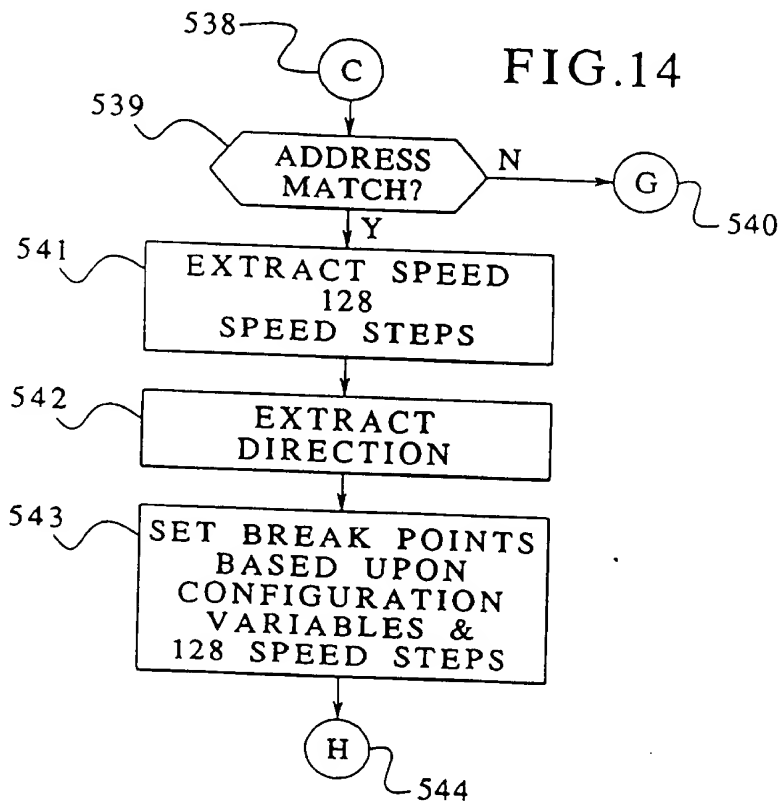


FIG.15

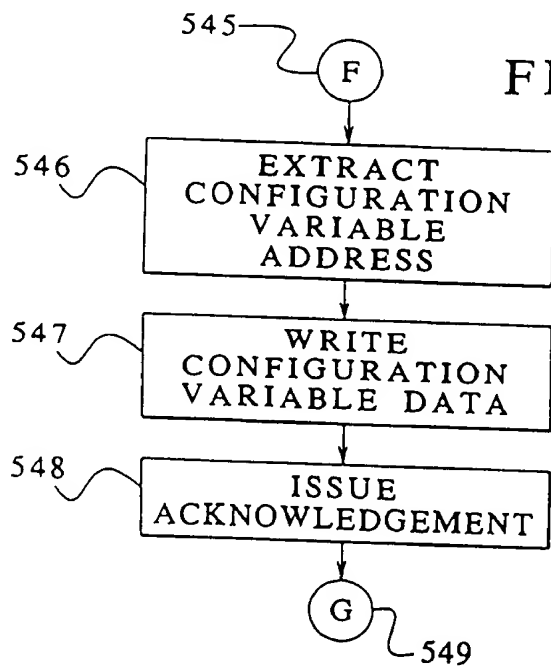


FIG.16

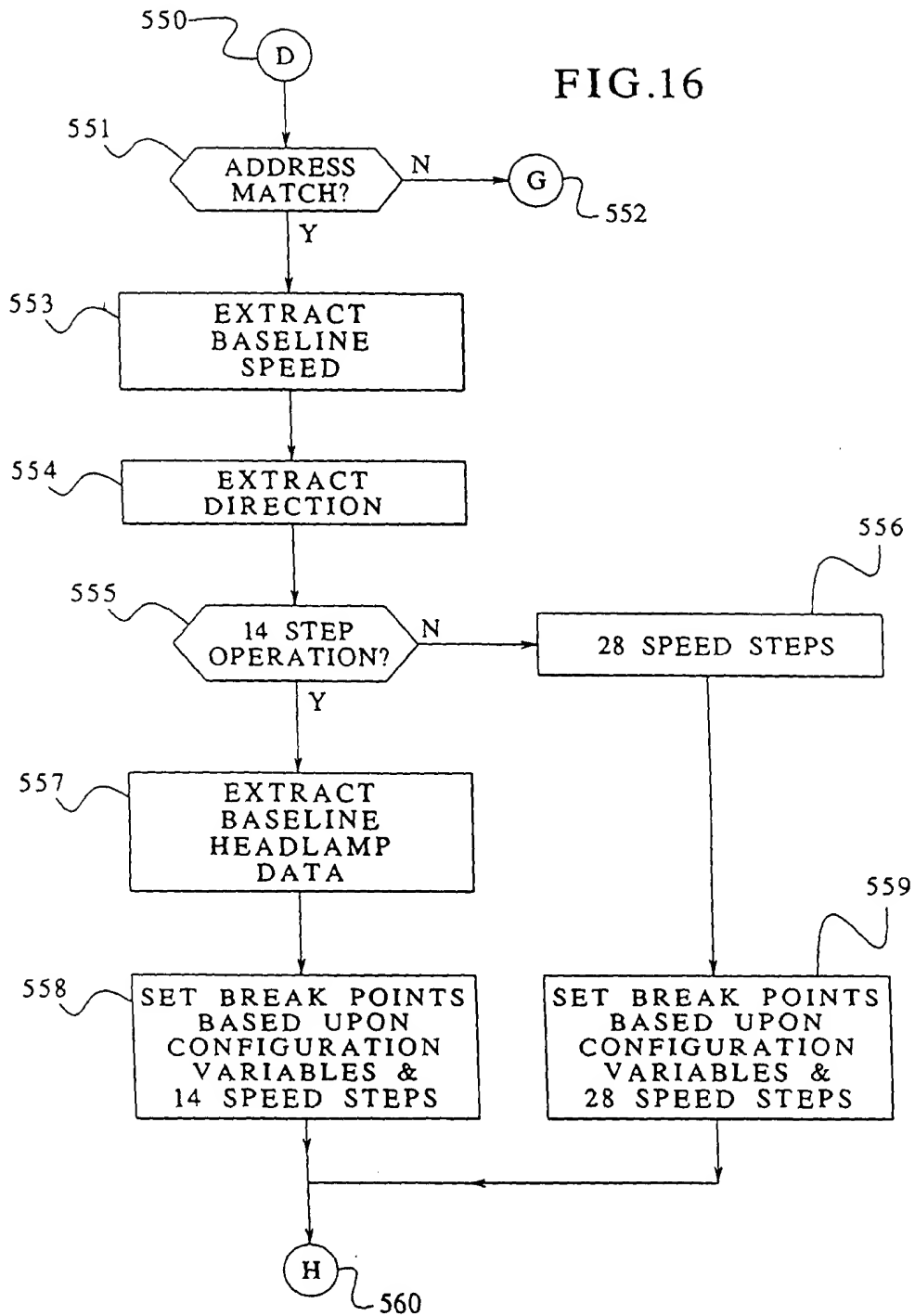


FIG.17

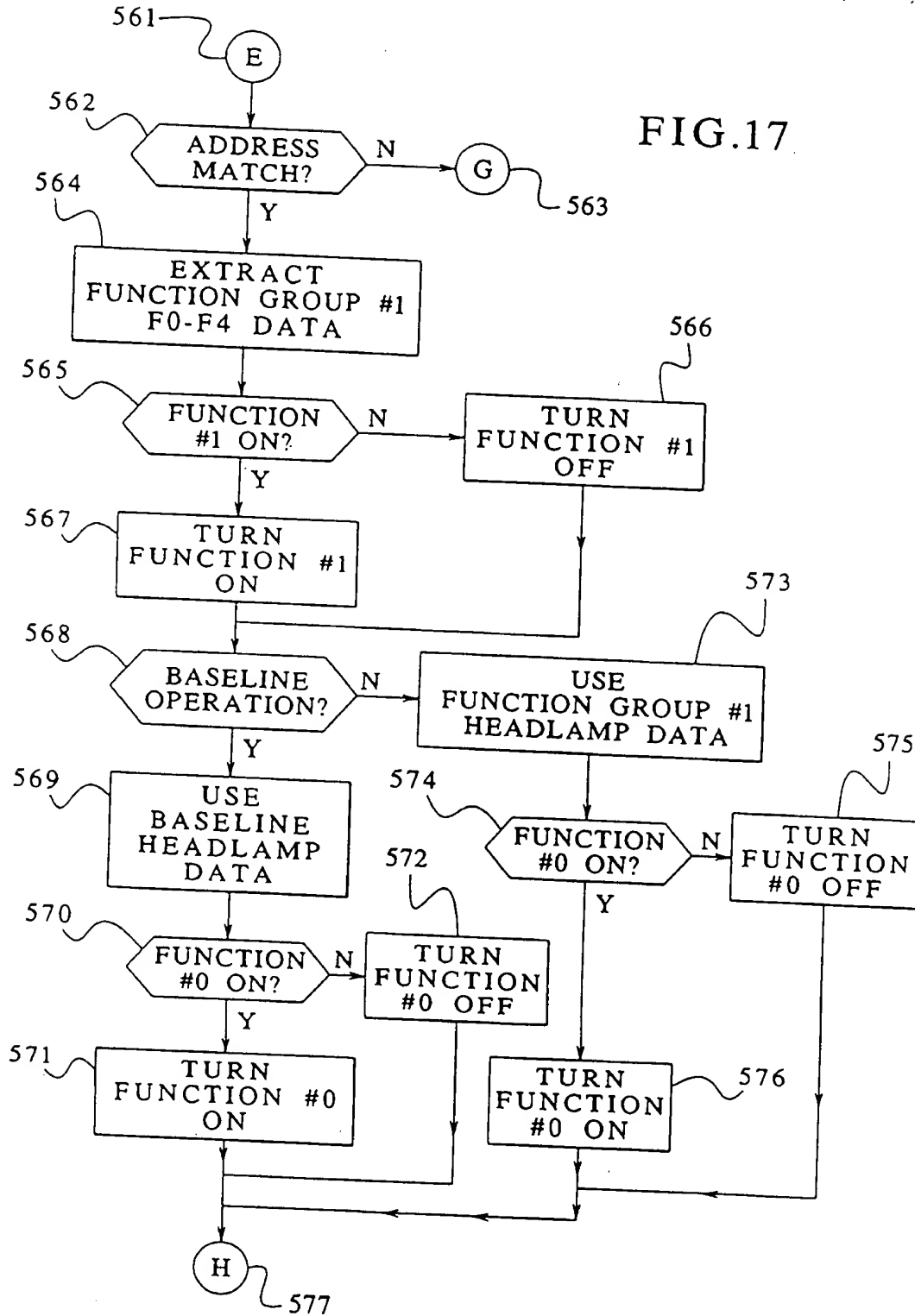
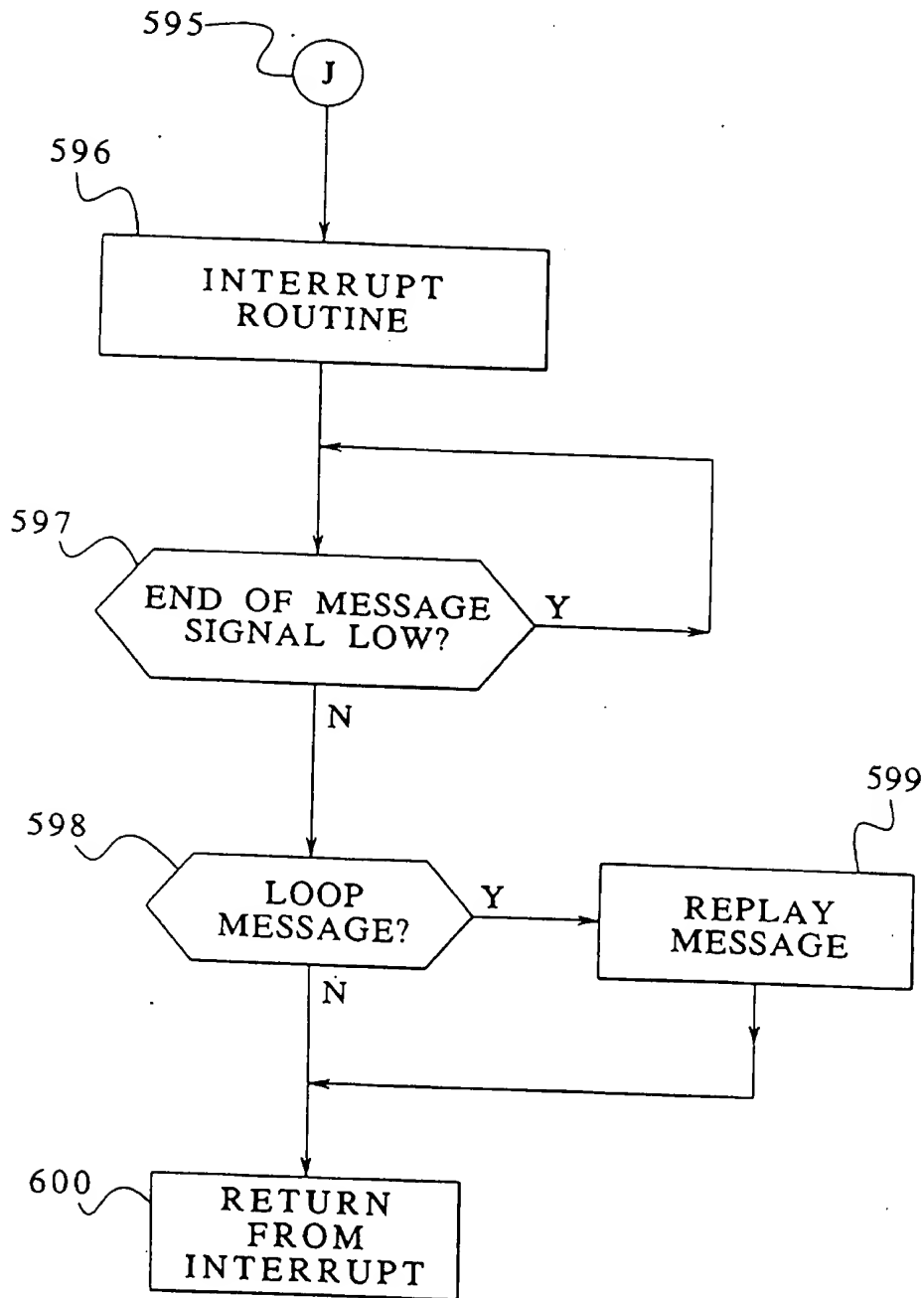


FIG.18



000001-1542560

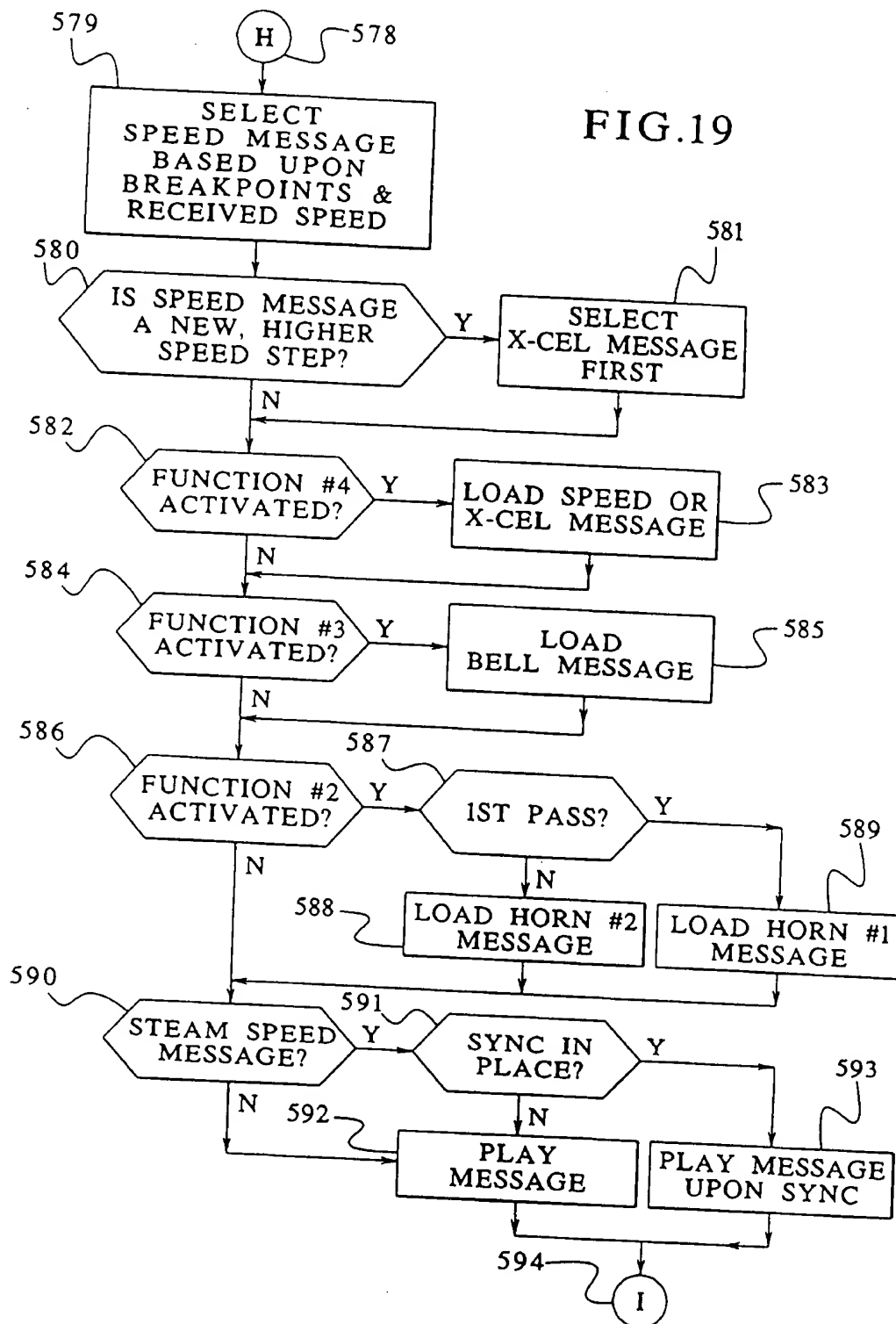


FIG.20

